

SCHEME OF INSTRUCTION & EXAMINATION
SECOND YEAR (ELECTRICAL & ELECTRONICS ENGINEERING)
SEMISTER III

Sub Code	Subjects	L	T	P	Duration of Theory Exam	Marks Allotted				
						Th.	S	P	O	Total
EE 3.1	Engineering Mathematics III	3	1	-	3	100	25	-	-	125
EE 3.2	Digital Integrated Circuits	3	1	2	3	100	25	50	-	175
EE 3.3	Electrical Circuit Analysis & Synthesis	3	1	-	3	100	25	-	-	125
EE 3.4	Electrical Machines I	3	1	2	3	100	25	-	-	125
EE 3.5	Electronic Devices & Circuits	3	1	2	3	100	25	50	-	175
EE 3.6	Electrical measurement & Instruments	3	1	2	3	100	25	-	-	125
	TOTAL	18	06	8	18	600	150	100		850

SCHEME OF INSTRUCTION & EXAMINATION
SECOND YEAR (ELECTRICAL & ELECTRONICS ENGINEERING)

SEMISTER IV

Sub Code	Subjects	L	T	P	Duration of Theory Exam	Marks Allotted				
						Th.	S	P	O	Total
EE 4.1	Numerical Techniques and Programming	3	1	2	3	100	25	-	-	125
EE 4.2	Linear Integrated Circuits	3	1	2	3	100	25	50	-	175
EE 4.3	Electrical Power	3	1	-	3	100	25	-	-	125
EE 4.4	Electrical machines II	3	1	2	3	100	25	50	-	175
EE 4.5	Analog and Digital Communication	3	1	2	3	100	25	-	-	125
EE 4.6	Economic Management	3	-	-	3	100	25	-	-	125
	TOTAL	18	06	8	18	600	150	100	-	850

SCHEME OF INSTRUCTION & EXAMINATION

THIRD YEAR (ELECTRICAL & ELECTRONICS ENGINEERING)

SEMISTER V

Sub Code	Subjects	L	T	P	Duration of Theory Exam	Marks Allotted			
						Th.	S	P	Total
EE 5.1	Electromagnetic Theory	3	1	-	3 Hrs	100	25	-	125
EE 5.2	Electrical Machines III	3	1	2	3 Hrs	100	25	50	175
EE 5.3	Design of Electronic Circuits	3	1	-	3 Hrs	100	25	-	125
EE 5.4	Control Engineering	3	1	2	3 Hrs	100	25	-	125
EE 5.5	Microprocessor and Interfacing	3	1	2	3 Hrs	100	25	50	175
EE 5.6	Electronic Instrumentation	3	1	2	3 Hrs	100	25	-	125
	TOTAL	18	6	8		600	150	100	850

- ❖ Practical examination I shall consist of experiments from subjects EE 5.2 and EE 5.4.
- ❖ Practical examination II shall consist of experiments from subjects EE 5.5 and EE 5.6.

SEMISTER VI

Sub Code	Subjects	L	T	P	Duration of Theory Exam	Marks Allotted			
						Th.	S	P	Total
EE 6.1	Power Electronics	3	1	2	3 Hrs	100	25	50	175
EE 6.2	Digital Signal Processing.	3	1	-	3 Hrs	100	25	--	125
EE 6.3	Electrical Power System I	3	1	-	3 Hrs	100	25	--	125
EE 6.4	Electrical Drives and Control	3	1	2	3 Hrs	100	25	50	175
EE 6.5	Embedded Systems	3	1	2	3 Hrs	100	25	--	125
EE 5.6	Electrical Machine Design	3	1	2	3 Hrs	100	25	--	125
	TOTAL	18	6	8		600	150	100	850

- ❖ Practical examination I shall consist of experiments from subjects EE 6.1 and EE 6.5.
- ❖ Practical examination II shall consist of experiments from subjects EE 6.4 and EE 6.6.

SCHEME OF INSTRUCTION & EXAMINATION

FINAL YEAR (ELECTRICAL & ELECTRONICS ENGINEERING)

SEMISTER VII

Sub Code	Subjects	L	T	P	Durati on of Theory Exam	Marks Allotted				
						Th.	S	P	O	Total
7.1	VLSI Circuit Design	3	1	2	3	100	25	-	50	175
7.2	Electrical Power System II	3	1	-	3	100	25	-	-	125
7.3	Advanced Controlled Drives	3	1	2	3	100	25	-	-	125
7.4	Elective I	3	1	2	3	100	25	-	50	175
7.5	Elective II	3	1	2	3	100	25	-	50	175
7.6	Project	-	-	3	-	-	25	-	50	75
	TOTAL	15	5	12	-	500	150	-	200	850

Elective I	Elective II
7.4.1 Power System Deregulation	7.5.1 Engineering Design
7.4.2 Switch Mode Power Conversion	7.5.2 Special Electric Machines
7.4.3 Electrical Estimation and costing	7.5.3 Flexible AC Transmission System
7.4.4 Advance Digital Signal Processing	7.5.4 Data Communication and Networking
7.4.5 Neural Networks and Fuzzy logic	7.5.5 Industrial Robotics
7.4.6 Data Base Management System	7.5.6 Satellite Communication

SCHEME OF INSTRUCTION & EXAMINATION

B.E. FOURTH YEAR (ELECTRICAL & ELECTRONICS ENGINEERING)

SEMISTER VIII

Sub Code	Subjects	L	T	P	Durati on of Theory Exam	Marks Allotted				
						Th.	S	R	O	Total
8.1	High Voltage Engineering	3	1	2	3	100	25	-	50	175
8.2	Principles of Industrial Engineering	3	-	-	3	100	25	-	50	175
8.3	Elective III	3	1	2	3	100	25	-	50	175
8.4	Elective IV	3	1	2	3	100	25	-	50	175
8.5	Project	-	-	10	-	-	50	50	50	150
	TOTAL	12	3	16	-	400	150	50	250	850

Elective III	Elective IV
8.3.1 Transient over voltages in Power system	8.4.1 Power Quality
8.3.2 Energy Engineering and Management	8.4.2 Image Processing and machine vision
8.3.3 Testing & Commissioning of Electrical Equipmen	8.4.3 Wind and PV Electrical Energy System
8.3.4 Projective Static relays	8.4.4 Biomedical Instrumentation
8.3.5 Cryptography And Network Security	8.4.5 Optimization Techniques
8.3.6 Optical fiber Communications	8.4.6 Illumination Engineering